

液晶模组说明书

LCD Module Instructions

初定规格 Preliminary specification

正式规格 Official specifications

| | |
|-----------------------------|---|
| 项目编号 Project No. | TFT-H062A02 系列 |
| 产品描述 Product Description | TFT LCD Module 1024 (RGB) x 600 Pixels 6.2 Inch TFT LCD |

客户确认签章:

Signature by customer:

小批量试产 Trial Production 大批量生产 Mass Production

请返回一份带有您的签名和评论的确认副本

Please return one copy confirmation with signature and your comments

HotDisplay Technology Co., Ltd

鑫洪泰科技（广东）有限公司

深圳市鑫洪泰电子科技有限公司

广东省东莞市凤岗镇东深公路凤岗段 208 号天安数码城 N3 栋 3 楼

免责声明

HotDisplay, HOTLCD 和 HOTHMI 是深圳市鑫洪泰电子科技有限公司的商标, HOTHMI 拥有多项专利、商标、商业秘密和其它知识产权。

HOTHMI 对公司产品提供可靠性数据(包括数据表)、设计资料(包括参考设计)、应用 或其它设计建议、技术支持和其它资源, 但不就本司任何产品用于任何特定目的做出担保。HOTHMI 不承担任何因产品的使用产生责任, 包括使用方须遵守的法律法规和安全使用标准。

对于在规格书中提到的产品参数, 在不同的应用条件下实际性能可能会产生变化。任何参数的配置和使用必须经由客户的技术支持进行验证, 对本文档所涉及的内容进行变更, 恕不另行通知。

HOTHMI 对您的使用授权仅限于产品的应用, 除此之外不得复制或展示所述资源, HOTHMI 也不提供任何人或第三方机构的知识产权授权许可。如因使用所述资源而产生任何索赔、赔偿、成本、债务及任何损失, HOTHMI 对此概不负责, 并且您须赔偿由此对 HOTHMI 造成的损害。

HOTHMI 所提供产品均受 HOTHMI 的销售条款以及 www.hotlcd.com 上或随附 HOTHMI 产品提供的其他可适用条款的约束。HOTHMI 提供所述资源并不扩展或以其他方式更改 HOTHMI 针对 HOTHMI 产品所发布的可适用的担保范围或担保免责声明。

HOTHMI 反对并拒绝您可能提出的任何其他或不同的条款。

Disclaimers

HotDisplay, HOTLCD and HOTHMI are trademarks of Shenzhen HOTHMI Electronic Technology Co., Ltd. and HOTHMI owns several patents, trademarks, trade secrets and other intellectual property rights.

HOTHMI provides reliability data (including data sheets), design information (including reference designs), application or other design advice, technical support and other resources for the Company's products, but does not warrant the use of any of its products for any particular purpose. HOTHMI assumes no liability arising from the use of its products, including compliance by the user with laws, regulations and standards for safe use.

For the product parameters mentioned in the specification, the actual performance may vary under different application conditions. The configuration and use of any parameter must be verified by the customer's technical support. The content of this document is subject to change without notice.

HOTHMI's license to use you is limited to the application of the product and no other reproduction or display of said resources is permitted, nor does HOTHMI provide a license to the intellectual property of any person or third party organization. HOTHMI shall not be liable for any claims, damages, costs, liabilities and any losses arising out of the use of said resources and you shall indemnify HOTHMI for any damage caused thereby.

HOTHMI offers its products subject to HOTHMI's Terms of Sale and other applicable terms provided on or with HOTHMI products at www.hotlcd.com. HOTHMI provides the resources described and does not extend or otherwise modify the scope of applicable warranties or warranty disclaimers issued by HOTHMI with respect to HOTHMI products.

HOTHMI objects to and rejects any other or different terms that you may offer.

内容
substance

| | |
|------------------------------------|----|
| 一、基本特征 General Feature | 4 |
| 二、外形尺寸 Outline Dimensions | 5 |
| 三、引脚说明 Pin Description | 6 |
| 四、电气特性 Electrical Characteristics | 12 |
| 五、液晶光学规格 TFT OPTICAL SPECIFICATION | 15 |
| 六、交流特性 AC characteristic | 17 |
| 七、可靠性测试 RELIABILITY TEST | 27 |
| 八、处理和注意事项 HANDDLING & CAUTIONS | 29 |
| 九、初始化代码 Initialization code | 31 |

一、基本特征 General Feature:

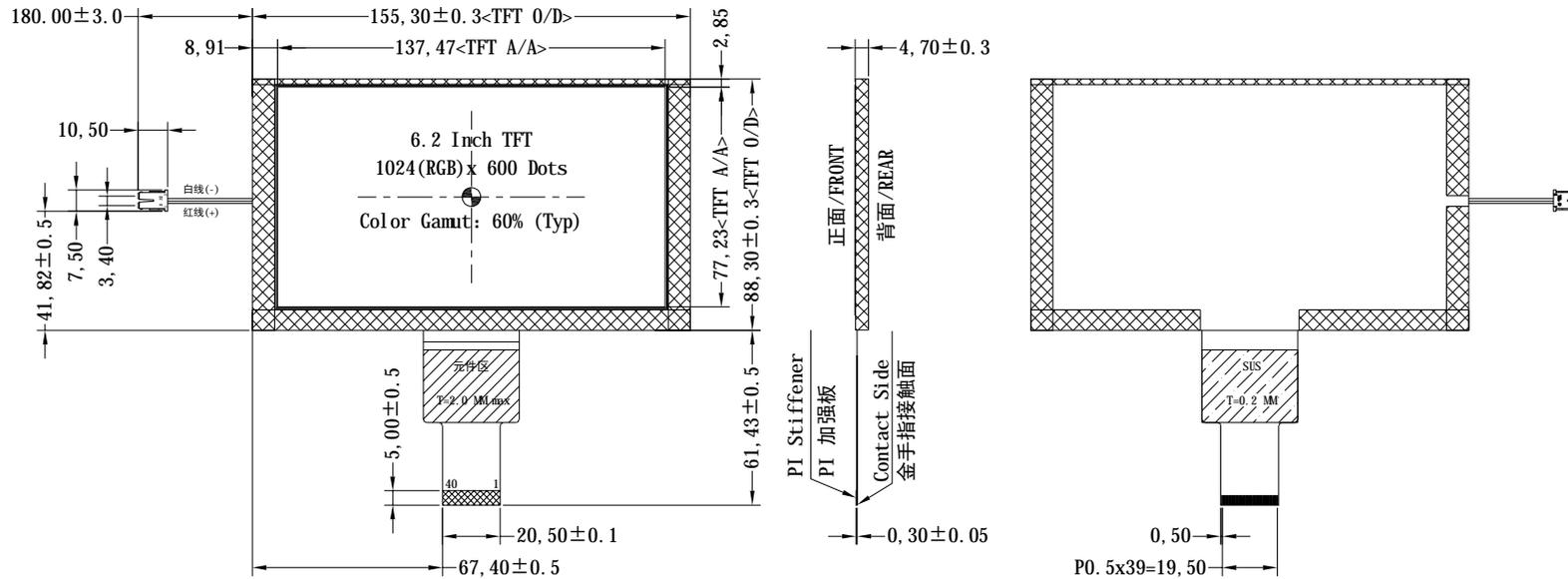
| 项目 Item | 标准值 Standard Value | | | 单位 Unit |
|---------------------------------------|--|--|--|-------------|
| TFT 显示尺寸 TFT Display Size | 6.2 | | | 英寸 Inch |
| TFT 分辨率 TFT Number of Pixels | 1024 (RGB) (H) x 600 (V) | | | 像素 Pixel |
| TFT 显示有效区域 TFT Display Active Area | 137.472 (H) x 77.232 (V) | | | 毫米 mm |
| TFT 模组外形尺寸 TFT Module Dimensions | 155.30 (H) x 88.30 (V) x 4.60 (D) <TFT> | | | 毫米 mm |
| TFT 观看方向 TFT Viewing Direction | U80/D80/R80/L80 (Typ.) | | | 度数 Deg. |
| TFT 模组接口 TFT Module Interfaces | LVDS interface (VESA/8Bit) MIPI Display Serial Interface (4 LANE) | | | - |
| TFT 驱动芯片 TFT Driver IC | JD9168S | | | - |
| 电容触摸屏驱动芯片 CTP Driver IC | - | | | - |
| 触摸屏 Touch Panel | <input checked="" type="checkbox"/> 不带触摸屏 No TP | <input type="checkbox"/> 带容触摸屏 Have CTP | <input type="checkbox"/> 带玻璃盖板 Have Glass cover | - |
| TFT 模组重量 TFT Module Weight | 不带触摸屏 Without TP | T. B. D ±5% | | 克 g |
| | 带电容触摸屏 With CTP | T. B. D ±5% | | |
| | 带电阻触摸屏 With RTP | T. B. D ±5% | | |

说明 Description:

***为防止LCD产生图像残留，在使用常黑面板时不要长时间显示固定图案。如果LCD需要显示固定图案，建议时间少于 2 分钟或者更短的时间内刷新显示内容一次或多次。
 ***In order to prevent the LCD from producing image retention, do not display a fixed pattern for a long time when using a normally black panel. If the LCD needs to display a fixed pattern, it is recommended to refresh the display one or more times in less than 2 minutes or less.
 强烈建议使用黑色图像或移动图像作为屏幕保护程序。It is strongly recommended to use a black image or moving image as a screen saver.

字体：黑体
 bold (typeface)

| Pin_SIGNAL | | Pin_SIGNAL | |
|------------|---------|------------|---------|
| 1 | NC/LEDK | 21 | DB0 |
| 2 | NC/LEDA | 22 | DB1 |
| 3 | GND | 23 | DB2 |
| 4 | VCI | 24 | DB3 |
| 5 | DR0 | 25 | DB4 |
| 6 | DR1 | 26 | DB5 |
| 7 | DR2 | 27 | DB6 |
| 8 | DR3 | 28 | DB7 |
| 9 | DR4 | 29 | GND |
| 10 | DR5 | 30 | PCLK |
| 11 | DR6 | 31 | DISP |
| 12 | DR7 | 32 | HSD |
| 13 | DG0 | 33 | VSD |
| 14 | DG1 | 34 | DEN |
| 15 | DG2 | 35 | NC |
| 16 | DG3 | 36 | GND |
| 17 | DG4 | 37 | REST |
| 18 | DG5 | 38 | SPI-CS |
| 19 | DG6 | 39 | SPI-SCL |
| 20 | DG7 | 40 | SPI-SDI |



| | | | |
|--------------------------------|--|--------------------------------|------------------------------------|
| 液晶显示类型 LCD Display Type | 薄膜晶体管/透射式/常黑/16.7M色 TFT/Transmissive/Normally Black/16.7M COLOR | | |
| 观看方向/角度 Viewing Direction | 80/D80/R80/L80 | TFT 储存温度 TFT Storage Temp | -30 °C ~ 80 °C |
| 接口 Interface | RGB+SPI | 驱动芯片 Driver IC | JD9168<TFT>或兼容芯片 |
| 工作电压 Operating Voltage | VCI=3.0~3.3V(Type) | 背光驱动条件 Backlight Conditions | Vf=9.0 ~ 9.6V, If=280mA |
| TFT 工作温度 TFT Operating Temp | -30 °C ~ 80 °C | 液晶模组亮度 LCM Brightness | 900 cd/m ² (Typ)/固定电流测试 |



| | | | |
|-----------------------|------------|----------------------------|----------|
| 产品型号 Product model | | TFT-H062A02WSIJDN40 | |
| 比例 Scale | 1:1 | 设计图 Design | Li u. YL |
| 日期 Date | 2024-04-28 | 公差 Tol. | ±0.3 |
| 版本 Ver | V0-0 | 单位 Unit | MM |

三、引脚说明 Pin Description

3.1 模组引脚说明 TFT Pin Description

| 引脚编号 Pin NO. | 标号 Symbol | 详细描述 Description |
|--------------------------|--------------|---|
| 1 | LEDA | LED阳极 LED anode |
| 2 | LEDK | LED阴极 LED cathode |
| 3 | GND | 电源地 Power supply ground |
| 4 | D3P | MIPI/LVDS 数据通道 3 正端输入引脚 MIPI/LVDS data Lane 3 positive-end input pin |
| 5 | D3N | MIPI/LVDS 数据通道 3 负端输入引脚 MIPI/LVDS data Lane 3 negative-end input pin |
| 6 | GND | 电源地 Power supply ground |
| 7 | D2P | MIPI/LVDS 数据通道 2 正端输入引脚 MIPI/LVDS data Lane 2 positive-end input pin |
| 8 | D2N | MIPI/LVDS 数据通道 2 负端输入引脚 MIPI/LVDS data Lane 2 negative-end input pin |
| 9 | GND | 电源地 Power supply ground |
| 10 | DCKP | MIPI/LVDS 时钟线正端输入引脚 MIPI/LVDS clock Lane positive-end input pin |
| 11 | DCKN | MIPI/LVDS 时钟线负端输入引脚 MIPI/LVDS clock Lane negative-end input pin |
| 12 | GND | 电源地 Power supply ground |
| 13 | D1P | MIPI/LVDS 数据通道 1 正端输入引脚 MIPI/LVDS data Lane 1 positive-end input pin |
| 14 | D1N | MIPI/LVDS 数据通道 1 负端输入引脚 MIPI/LVDS data Lane 1 negative-end input pin |
| 15 | GND | 电源地 Power supply ground |
| - 下一页 - - Next Page - | | |

| 引脚编号 Pin NO. | 标号 Symbol | 详细描述 Description |
|-------------------|--------------|---|
| 16 | DOP | MIPI/LVDS 数据通道 0 正端输入引脚 MIPI/LVDS data Lane 0 positive-end input pin |
| 17 | DON | MIPI/LVDS 数据通道 0 负端输入引脚 MIPI/LVDS data Lane 0 negative-end input pin |
| 18 | GND | 电源地 Power supply ground |
| 19 | RESX | 全局复位引脚。低电平进入复位状态 Global reset pin. Active Low to enter Reset State. |
| 20~23 | NC | 不连接 Not connected |
| 24~25 | VCI | 数字电源电压 Digital supply voltage |
| 26 | NC | 不连接 Not connected |
| 27 | GND | 电源地 Power supply ground |
| 28~30 | NC | 不连接 Not connected |
| - 结束 - - END - | | |

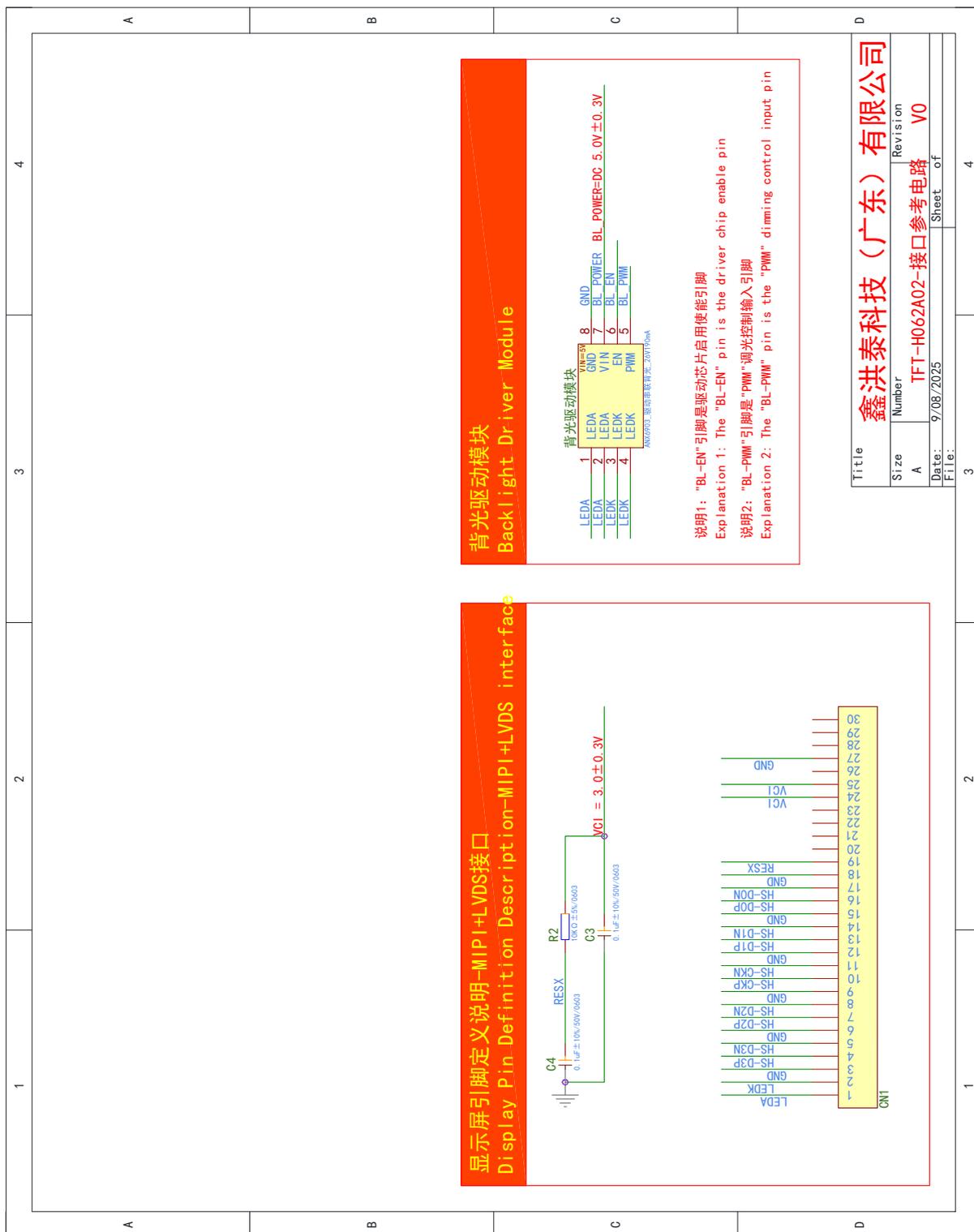
3.2 电容触摸引脚说明 CTP Pin Description

| 引脚编号 Pin NO. | 标号 Symbol | 详细描述 Description |
|-------------------|--------------|---------------------|
| 1 | - | - |
| 2 | - | - |
| 3 | - | - |
| 4 | - | - |
| 5 | - | - |
| 6 | - | - |
| - 结束 - - END - | | |

3.3 RTP 引脚说明 RTP Pin Description

| 引脚编号 Pin NO. | 标号 Symbol | 详细描述 Description |
|-------------------|--------------|---------------------|
| 1 | - | - |
| 2 | - | - |
| 3 | - | - |
| 4 | - | - |
| - 结束 - - END - | | |

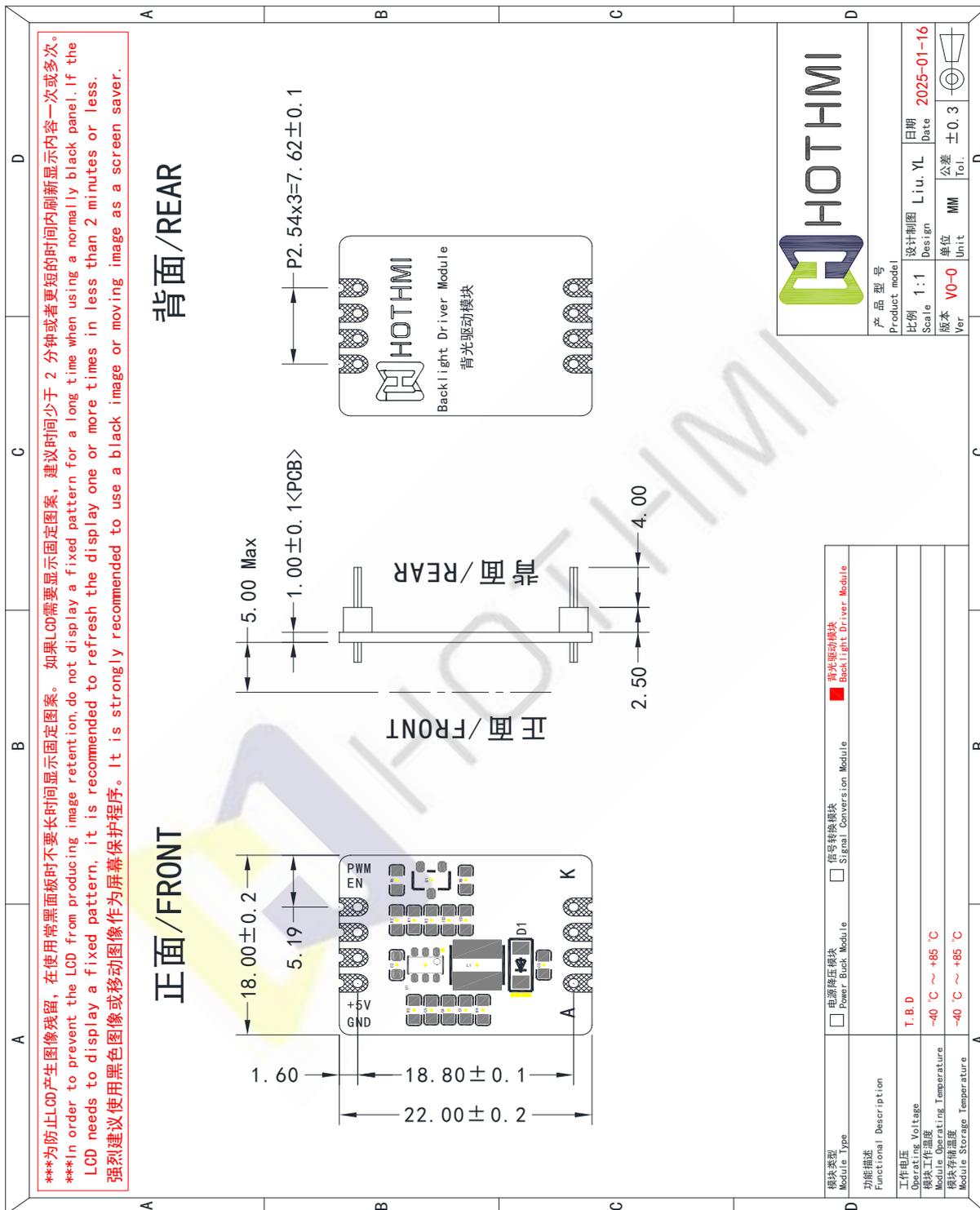
3.4 参考电路图 Reference Circuit Diagram



| | | | |
|-------|--------------------|-----------------|----|
| Title | | 鑫洪泰科技 (广东) 有限公司 | |
| Size | Number | Revision | |
| A | TFT-H062A02-接口参考电路 | V0 | |
| Date: | 9/08/2025 | Sheet | of |
| File: | | | |

说明 1: 为了方便调试, 我司可以提供配套的驱动模块

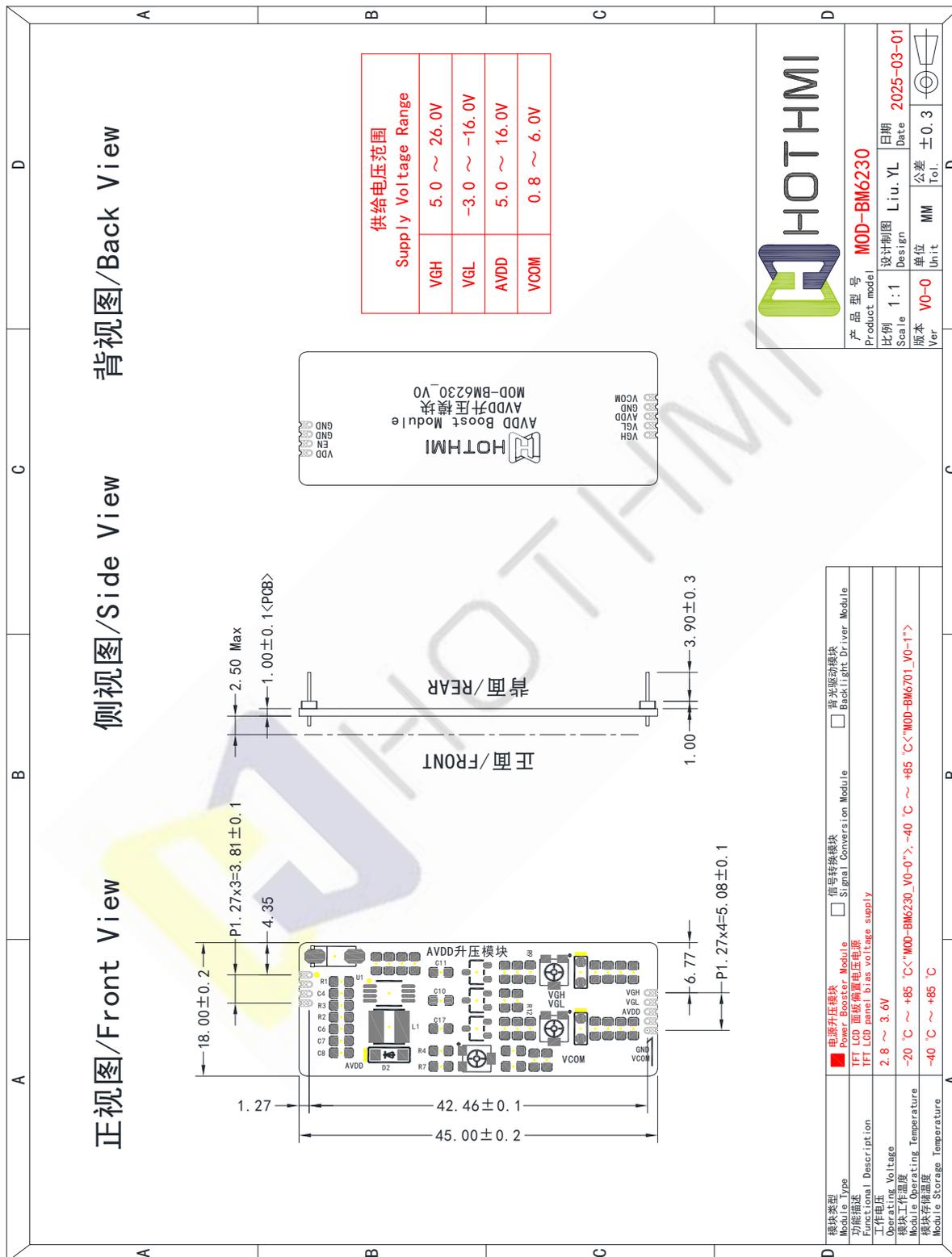
Note 1: In order to facilitate debugging, our company can provide matching drive Module



背光模块尺寸 Backlight Module Dimensions

说明 1: 我司所有背光模块长,宽尺寸相同,引脚定义相同。使用的模块以实物为准。

Note 1: All of our backlight modules have the same length and width dimensions and the same pin definition. Modules used are subject to physical.



AVDD 升压模块尺寸

AVDD Boost Module Dimensions

第 11 页 共 34 页

四、电气特性 Electrical Characteristics

4-1 TFT 模组工作条件 TFT LCD Module Operating Conditions

| 项目 Item | 标号 Symbol | 条件 Condition | 最小值 Min | 典型值 Type | 最大值 Max | 单位 Unit |
|---|-----------------|-----------------|--------------|-------------|--------------|------------|
| 数字电源 Digital Power | VCI | - | Type -0.3 | 3.0 | Type +0.3 | 伏 V |
| 接口工作电压 IO Supply Voltage | IOVCC | - | Type -0.1 | - | Type +0.1 | 伏 V |
| 模拟电路电源 Power For Analog | AVDD | - | Type -0.3 | - | Type +0.3 | 伏 V |
| TFT 栅极导通电压 TFT Gate ON Voltage | VGH | - | Type -1.0 | - | Type +1.0 | 伏 V |
| TFT 栅极关断电压 TFT Gate OFF Voltage | VGL | - | Type -1.0 | - | Type +1.0 | 伏 V |
| TFT 公共电极电压 TFT Common Electrode Voltage | VCOM | - | Type -0.5 | - | Type +0.5 | 伏 V |
| 工作电流 Operation Current | I _{oc} | - | - | - | - | 毫安 mA |
| 待机电流 Standby Current | I _{sc} | - | - | - | - | 微安 uA |
| TFT 工作温度 TFT Operating Temp | TOPR | - | -30 | +25 | +80 | 摄氏度 ℃ |
| TFT 储存温度 TFT Storage Temp | TSTG | - | -40 | +25 | +90 | 摄氏度 ℃ |

备注 Notes :

1. VGH 为 TFT 栅极工作电压。VGH is TFT Gate operating voltage.

2. VGL 为 TFT 栅极工作电压。VGL 信号的低电平必须与 Vcom 同相波动。

VGL is TFT Gate operating voltage. The low voltage level of VGL signal must be fluctuates with same phase as Vcom.

3. 必须调节 Vcom 以优化显示质量，如串扰和对比度等。

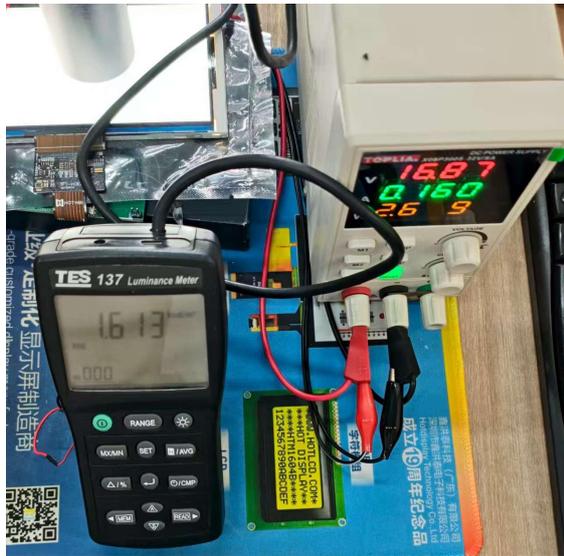
Vcom must be adjusted to optimize display quality, as Crosstalk and Contrast Ratio etc.

4. 该值只是参考值。客户可通过不同的 D-IC 优化设置值。

The value is just the reference value. The customer can optimize the setting value by the different D-IC.

4-2 背光工作条件 LED back light specification

| 项目 Item | 标号 Symbol | 条件 Condition | 最小值 Min | 典型值 Type | 最大值 Max | 单位 Unit |
|------------------------------------|--------------|---------------------------|--------------|-------------|--------------|------------------------------|
| 工作电压 Forward voltage | VF | 恒流 constant current | Type -1.5 | 18.0 | Type +1.5 | 伏特 V |
| 工作电流 Forward current | IF | | - | 160 | - | 毫安 mA |
| 亮度 (带 LCD) Luminance (With LCD) | Lv | 不带触摸 No TP | Type -150 | 1600 | Type +150 | 坎德拉/平方米 cd/m ² |
| | | 带触摸屏 Have TP | - | - | - | |
| LED 寿命 LED life time | Hr | Ta=25±3 °C | 20,000 | 30,000 | - | 小时 Hour |



不带触摸 No TP

带触摸屏 Have TP

注释 Note:

1. LED 寿命 (Hr) 定义为在 Ta=25±3 °C, 上表所示的典型电压电流值条件下持续工作直至亮度低于 50% 的时间。

LED life time (Hr) can be defined as the time in which it continues to operate under the condition: Ta=25±3 °C, typical IL value indicated in the above table until the brightness becomes less than 50%.

2. 以上结果是按 MTBF 计算方式预估判定的 LED 失效时间, 实际测试 LED 在 Ta=25±3 °C 点亮 5000H, 亮度衰减 8%.

The above results are estimated and judged by the MTBF calculation method of the LED failure time. The actual test LED is lit for 5000H at Ta=25±3 °C, and the brightness decays by 8%.

4-3 电容触摸屏工作条件 CTP Operating Conditions

| 项目 Item | 标号 Symbol | 条件 Condition | 最小值 Min | 典型值 Type | 最大值 Max | 单位 Unit |
|-------------------------------|--------------|-----------------|--------------|-------------|--------------|------------|
| 表面硬度 Surface hardness | - | 铅笔 Pencil | - | 6H | - | 硬度 Hard |
| 有效数据数 No. of valid data | - | - | - | - | 5 | 点 Point |
| 透光率 Transmittance | - | - | 80 | - | - | 百分比 % |
| 数字电源 Digital Power | VDD | - | Type -0.3 | 3.0 | Type +0.3 | 伏特 V |
| I/O 电源 I/O Power | IOVDD | - | Type -0.3 | 3.0 | Type +0.3 | 伏特 V |
| 工作温度 Operation Temperature | - | 20%-90%RH; | -20 | +25 | +70 | 摄氏度 ℃ |
| 储存温度 Storage Temperature | - | | -30 | +25 | +80 | 摄氏度 ℃ |

五、液晶光学规格 TFT OPTICAL SPECIFICATION

5.1 概述 Overview

光学规格的测试应在暗室（环境亮度 1lux，温度=25 2°C）中使用亮度计系统（测角仪系统和TOPCON BM-5）设备进行测量，测试单元应位于大约在 θ 和 Φ 等于 0 的视角下，距 LCD 表面 50cm 的距离。显示面上测量点的中心应保持固定。测量前背光应工作 30 分钟。

The test of Optical specifications shall be measured in a dark room (ambient luminance 1lux and temperature = 25 2°C) with the equipment of Luminance meter system (Goniometer system and TOPCON BM-5) and test unit shall be located at an approximate distance 50cm from the LCD surface at a viewing angle of θ and Φ equal to 0 . The center of the measuring spot on the Display surface shall stay fixed. The backlight should be operating for 30 minutes prior to measurement.

5.2 光学规格 Optical Specifications

| 参数 Parameter | | 标号 Symbol | 条件 Condition | 最小值 Min. | 典型值 Typ. | 最大值 Max. | 单位 Unit | 备注 Remark |
|--|------------------|--------------|--------------------|-------------|-------------|-------------|------------|-----------------------------|
| 视角范围 Viewing Angle Range | 水平 Horizontal | ⊕ 左/L | GR>10 | 68 | 80 | - | Deg. | Note 1 |
| | | ⊕ 右/R | | 68 | 80 | - | Deg. | |
| | 垂直 Vertical | ⊕ 上/U | | 68 | 80 | - | Deg. | |
| | | ⊕ 下/D | | 68 | 80 | - | Deg. | |
| 对比度 Contrast ratio | | CR | ⊕ = 0° | - | 1000 | - | - | Note2 |
| 色域 Color Gamut | | CG | CIE1931 | - | 60 | - | % | |
| 白色色度 White Chromaticity | | Wx | ⊕ = 0° | -0.03 | 0.309 | +0.03 | - | Note4 (Based on C Light) |
| | | Wy | | | 0.348 | | - | |
| 色彩还原 Reproduction of color | 红 Red | Rx | | | - | | - | |
| | | Ry | | | - | | - | |
| | 绿 Green | Gx | | | - | | - | |
| | | Gy | | | - | | - | |
| | 蓝 Blue | Bx | | | - | | - | |
| | | By | | | - | | - | |
| 响应时间（上升 + 下降） Response Time (Rising + Falling) | | Tr+Tf | ⊕ = 0° Ta= 25°C | - | 30 | 40 | ms | Note5 |

注释 Note:

1. 视角是对比度大于10的角度。视角确定为相对于光轴的水平或3、9点钟方向和垂直或6、12点钟方向 垂直于 LCD 表面（见图 1）。

Viewing angle is the angle at which the contrast ratio is greater than 10. The viewing angles are determined for the horizontal or 3, 9 o' clock direction and the vertical or 6, 12 o' clock direction with respect to the optical axis which is normal to the LCD surface (see FIGURE 1).

2. 对比度测量应在 $\theta = 0$ 的视角和 LCD 表面的中心进行。亮度测量时，视场中的所有像素首先设置为白色，然后设置为暗（黑色）状态。（参见图 1）亮度对比度（CR）是通过数学定义的。

Contrast measurements shall be made at viewing angle of $\Theta = 0$ and at the center of the LCD surface. Luminance shall be measured with all pixels in the view field set first to white, then to the dark (black) state. (see FIGUR 1) Luminance Contrast Ratio (CR) is defined mathematically.

$$CR = \frac{\text{Luminance when displaying a white raster}}{\text{Luminance when displaying a black raster}}$$

3. 透射率是没有 APF 和没有 CG 的值。

Transmittance is the Value without APF and without CG.

4. 上表中规定的色度坐标应由所有像素首先测量的光谱数据计算为红色、绿色、蓝色和白色。测量应在面板的中心进行。

The color chromaticity coordinates specified in the above table shall be calculated from the spectral data measured with all pixels first in red, green, blue and white. Measurements shall be made at the center of the panel.

5. 电光响应时间测量应如图 2 所示，通过打开和关闭“数据”输入信号来进行。亮度从 10%变化到90%所需的时间是 T_r ，90%到10%是 T_f 。

The electro-optical response time measurements shall be made as FIGURE 2 by switching the “data” input signal ON and OFF. The times needed for the luminance to change from 10% to 90% is T_r , and 90% to 10% is T_f .

Figure1 Measurement Set Up

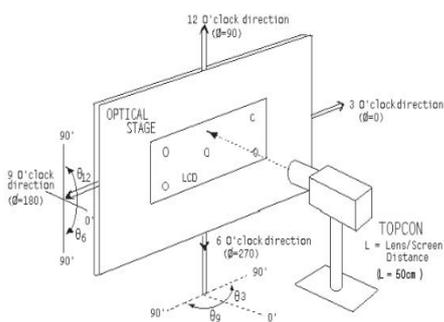


图 1

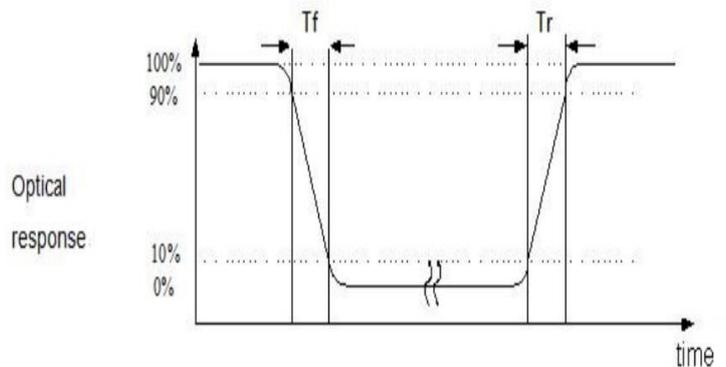
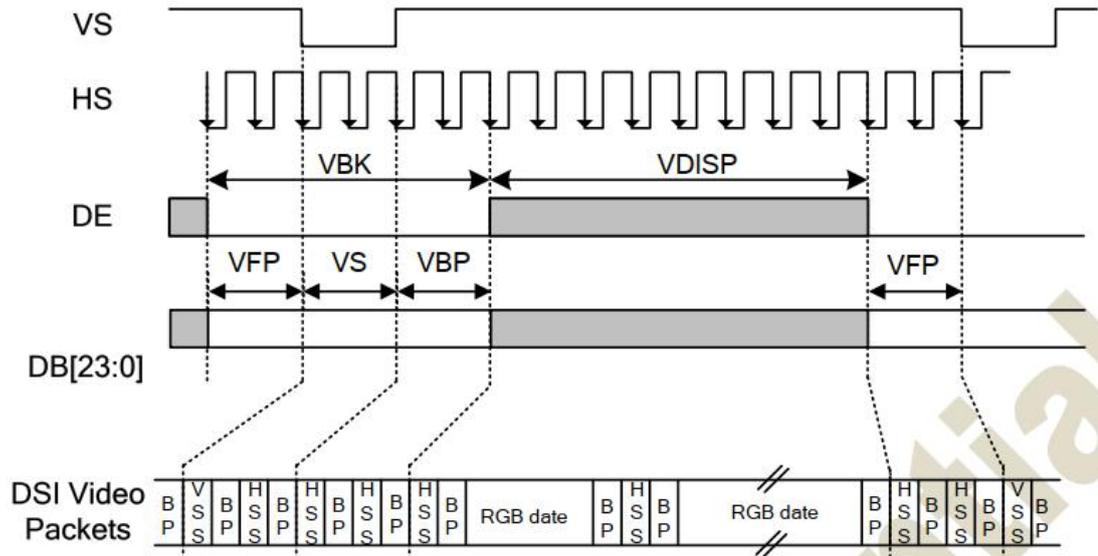


图 2

六、交流特性 AC characteristic

6.1. MIPI 交流特性 MIPI AC characteristic

Vertical Timings

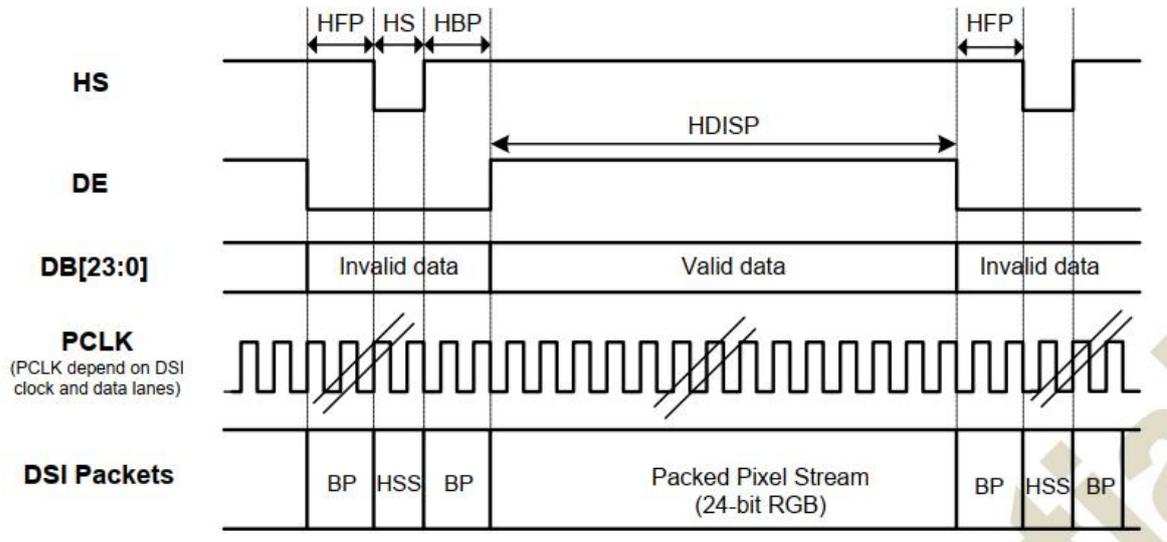


Resolution=1024x600 (T_A=25°C, IOVCC=3.3V, VCI=3.3V)

| Item | Symbol | Condition | Min. | Typ. | Max. | Unit |
|--------------------------|--------|------------|------|------|---------------------|------|
| Vertical low pulse width | VS | - | 2 | - | Note ⁽¹⁾ | Line |
| Vertical front porch | VFP | - | 2 | - | - | Line |
| Vertical back porch | VBP | - | 2 | - | Note ⁽¹⁾ | Line |
| Vertical blanking period | VBL | VS+VBP+VFP | 6 | - | - | Line |
| Vertical active area | - | VDISP | - | 600 | - | Line |
| Vertical Refresh rate | VRR | - | - | 60 | - | Hz |

Note: The VS and VBP pulse width are related to GIP start pulse and GIP clock pulse timing. The GIP start pulse and GIP clock pulse must be set at corresponding position for LCD normal display.

Horizontal Timings



RGB Resolution=1024x600 ($T_A=25^{\circ}\text{C}$, $V_{CCH}=I_{OVCC}=3.3\text{V}$, $V_{CI}=3.3\text{V}$)

| Item | Symbol | Condition | Min. | Typ. | Max. | Unit |
|----------------------------|--------|-------------|------|--------------------|------|------|
| HS low pulse width | HS | - | 100 | - | - | ns |
| Horizontal back porch | HBP | - | 100 | - | - | ns |
| Horizontal front porch | HFP | - | 400 | - | - | ns |
| Horizontal blanking period | HBLK | HS+HBP+HFP | - | 900 ⁽¹⁾ | - | ns |
| Horizontal active area | HDISP | 1024 pixels | - | 12 ⁽²⁾ | - | us |

Note:

- Below time limitation will apply in all DSI speed range.
 - HS+HBP \geq 500ns.
 - HFP \geq 400ns.
- Base on frame rate = 60HZ,
 - VS=2, VBP=2 and VFP=2.

6.2. LVDS 接口 LVDS Interface

($T_A = -40 \sim 85 \text{ }^\circ\text{C}$)

| Item | Symbol | Condition | Min. | Typ. | Max. | Unit |
|---|--------------|-----------------------------------|------|------|------|---------------|
| Differential input high threshold voltage | R_{xVTH} | $R_{xVCM} = 1.2V$ | +0.1 | +0.2 | +0.3 | V |
| Differential input low threshold voltage | R_{xVTL} | | -0.3 | -0.2 | -0.1 | V |
| Input voltage range (singled-end) | R_{xVIN} | | 0.7 | - | 1.7 | V |
| Differential input common mode voltage | R_{xVCM} | $ VID = 0.2$ | 1 | 1.2 | 1.4 | V |
| Differential input impedance | ZID | | 80 | 100 | 125 | ohm |
| Differential input voltage | $ VID $ | | 0.2 | - | 0.6 | V |
| Differential input leakage current | I_{LCLVDS} | | -10 | - | +10 | μA |
| LVDS Digital Stand-by Current | I_{STLVDS} | Clock & all Functions are stopped | - | TBD | - | μA |

Table 11.3: LVDS DC characteristic

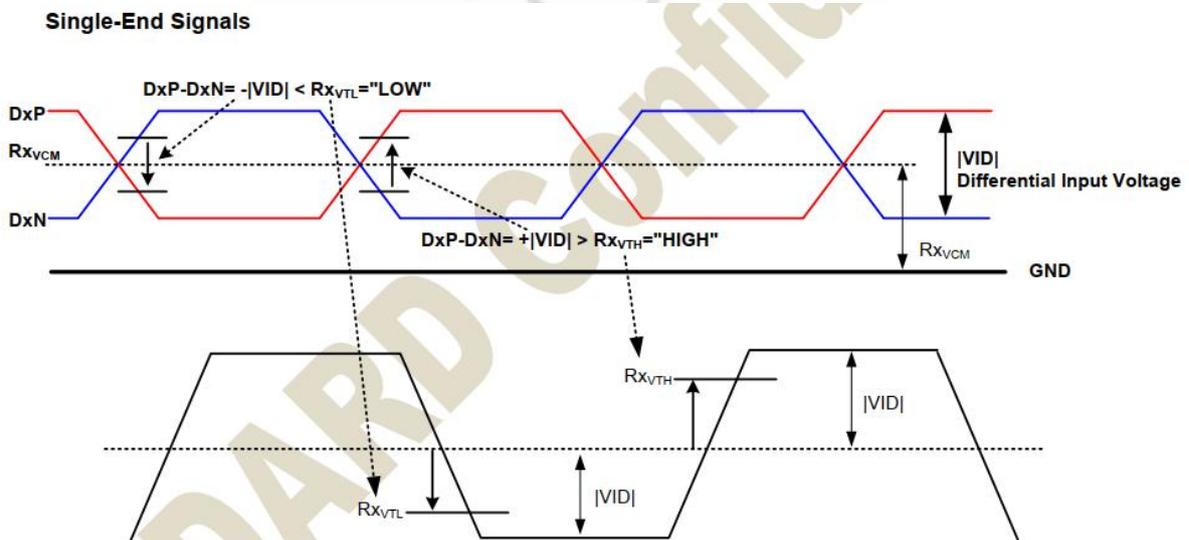


Figure 11.1: LVDS input timings

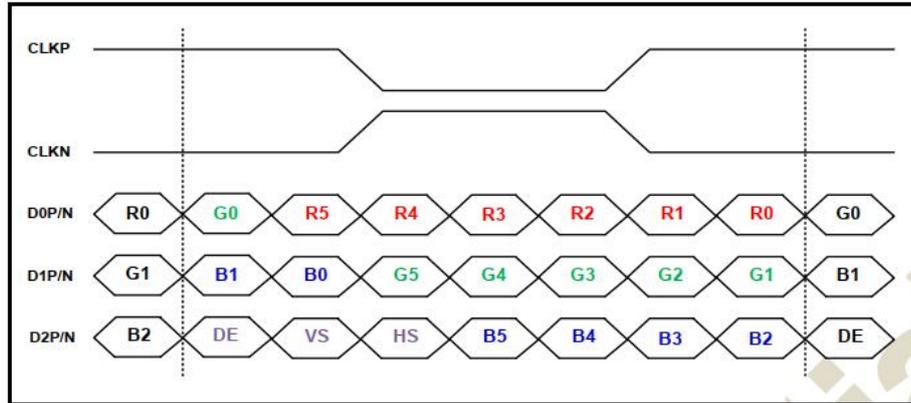


Figure 7.10: 6-bit LVDS input (IM[1:0]=01, LANSEL[1:0]=10, LVFMT=Don't care)

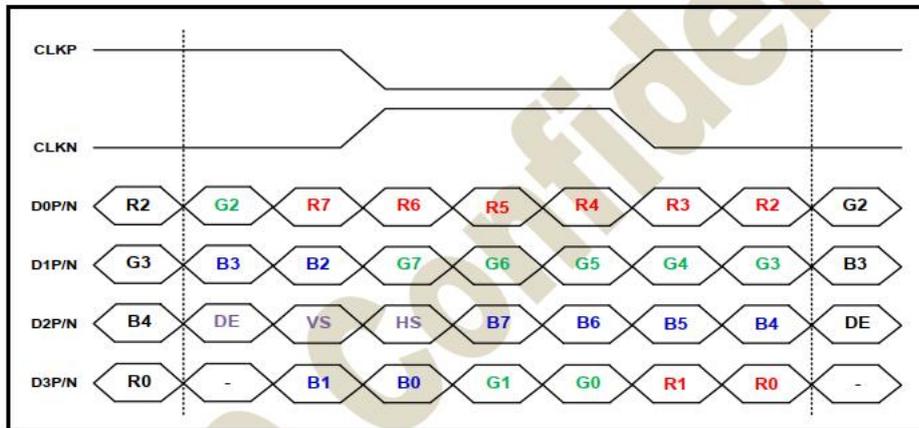


Figure 7.11: 8-bit LVDS input (IM[1:0]=01, LANSEL[1:0]=11, LVFMT=1(JEIDA))

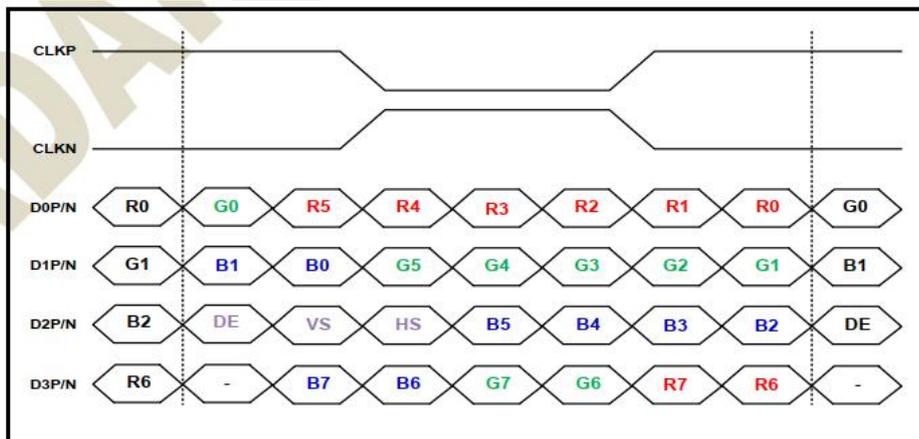
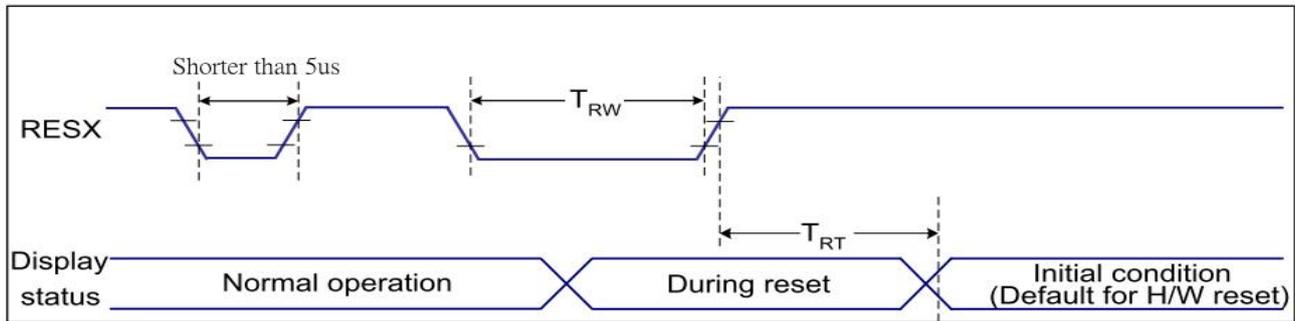


Figure 7.12: 8-bit LVDS input (IM[1:0]=01, LANSEL[1:0]=11, LVFMT=0(VESA))

6.3. 复位时序 Reset Timing



Reset Timing Characteristics IOVCC=1.65~1.95v Ta=25°C

| Parameter | Symbol | Specification | | | Unit |
|-----------------|--------------|---------------|------|------|------|
| | | Min. | Typ. | Max. | |
| Reset low width | Trst_width | 1 | - | - | ms |
| Reset time | Trst_time | 120 | - | - | ms |
| OP noise reject | Trst_op_nrij | - | - | 10 | us |

注释 Notes:

1. 复位取消还包括将 ID 字节、VCOM 设置和其他设置从 NVM（或类似设备）加载到寄存器所需的时间。每次在 RESX 上升沿后 5 ms 内有硬件复位取消时间（tRT）时，都会执行此加载。

The reset cancel includes also required time for loading ID bytes, VCOM setting and other settings from NVM (or similar device) to registers. This loading is done every time when there is HW reset cancel time (tRT) within 5 ms after a rising edge of RESX.

2. 根据下表，由于 RESX 线路上的静电放电引起的尖峰不会导致不规则的系统复位

Spike due to an electrostatic discharge on RESX line does not cause irregular system reset according to the table below:

| RESX Pulse | Action |
|---------------------|----------------|
| Shorter than 5us | Reset Rejected |
| Longer than 9us | Reset |
| Between 5us and 9us | Reset starts |

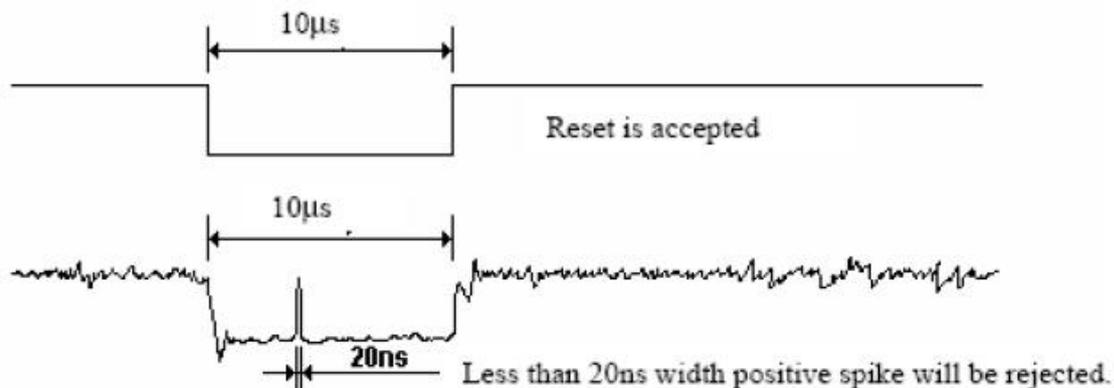
3. 在复位期间，显示屏将被消隐（显示屏进入消隐序列，最长持续时间为 120 毫秒，此时

Reset Starts in Sleep Out -mode。显示屏在 Sleep In -mode 中保持消隐状态。) 然后返回硬件复位的默认条件。

4. During the Resetting period, the display will be blanked (The display is entering blanking sequence, which maximum time is 120 ms, when Reset Starts in Sleep Out -mode. The display remains the blank state in Sleep In -mode.) and then return to Default condition for Hardware Reset.

4. 尖峰抑制也适用于有效复位脉冲期间，如下所示：

Spike Rejection also applies during a valid reset pulse as shown below:



5. 在进入睡眠期间应用复位时

When Reset applied during Sleep In Mode.

6. 在退出睡眠期间应用复位时

When Reset applied during Sleep Out Mode.

7. 释放 RESX 后需要等待 5 毫秒以上才能发送命令。也不能在 120 毫秒内发送睡眠退出命令。

It is necessary to wait 5msec after releasing RESX before sending commands. Also Sleep Out command cannot be sent for 120msec.

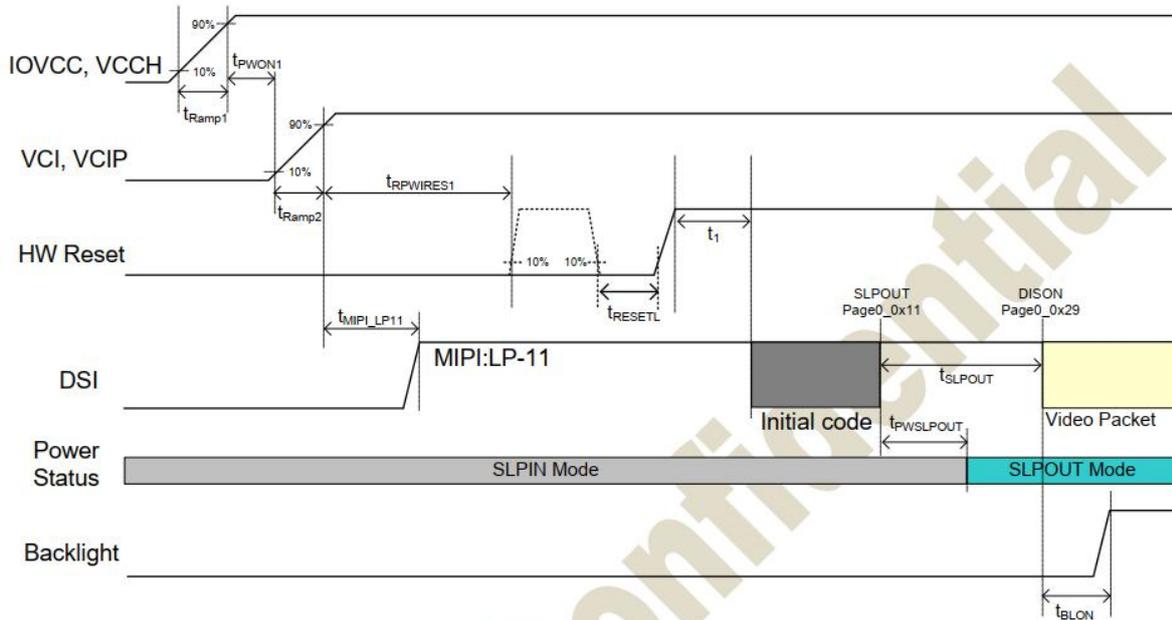
6.4. 电源开启/关闭顺序 Power On/Off Sequence

| Symbol | Value | | | Unit | Remark |
|------------------|-------|-----|----------------|---------|-----------------|
| | Min | Typ | Max | | |
| t_{PWON1} | 0 | 5 | - | ms | |
| t_{PWON2} | 0 | 5 | - | ms | |
| t_{PWON3} | 0 | 5 | | ms | |
| t_{PWON4} | 0 | 5 | | ms | |
| t_{ramp1} | 0.2 | - | 20 | ms | IOVCC, VCCH |
| t_{ramp2} | 0.2 | - | 20 | ms | VCI, VCIP, AVDD |
| t_{ramp3} | 0.2 | - | 20 | ms | AVEE |
| t_{ramp4} | 0.2 | - | - | ms | VGL |
| t_{ramp5} | 0.2 | - | - | ms | VGH |
| $t_{RPWIRES1}$ | 10 | - | - | ms | |
| $t_{RPWIRES2}$ | 1 | - | - | ms | |
| t_{MIPI_LP11} | - | - | $t_{RPWIRES1}$ | ms | |
| t_{RESETL} | 20 | - | - | μ s | |
| t_1 | 5 | - | - | ms | |
| t_{SLPOUT} | 120 | - | - | ms | |
| $t_{PWSLPOUT}$ | - | 45 | - | ms | |
| t_{BLON} | 2 | - | - | VS | |

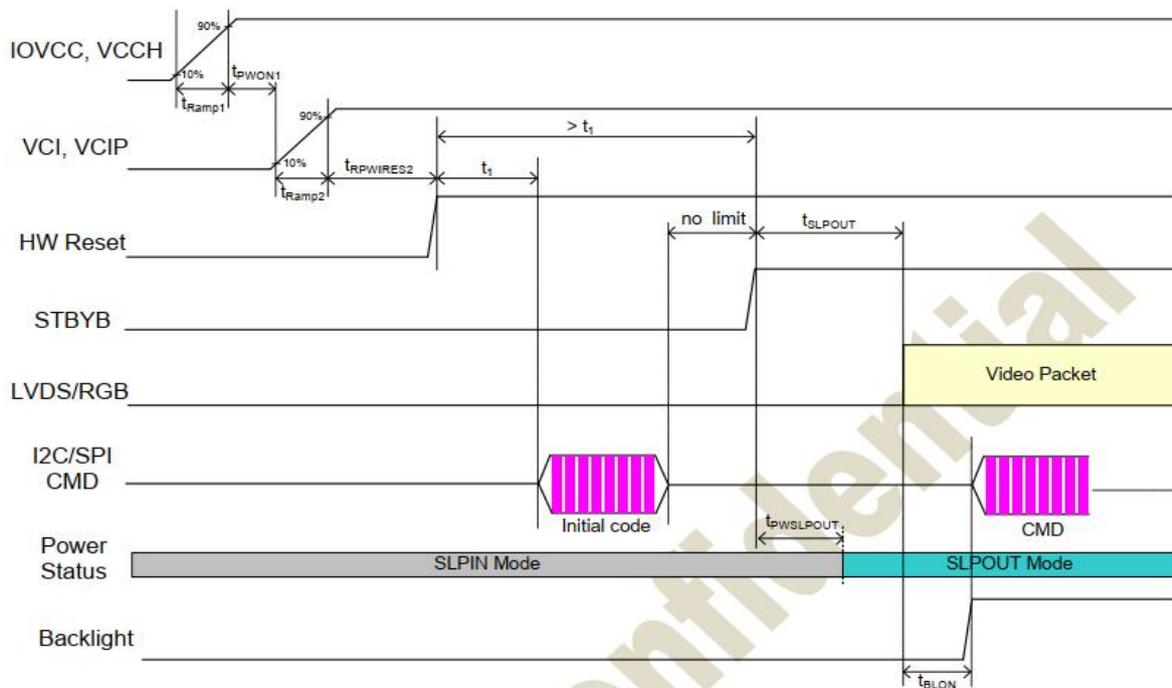
电源开启顺序框图

The power On sequence block diagram

2 power mode power on sequence – DSI:



2 power mode power on sequence – LVDS, RGB:

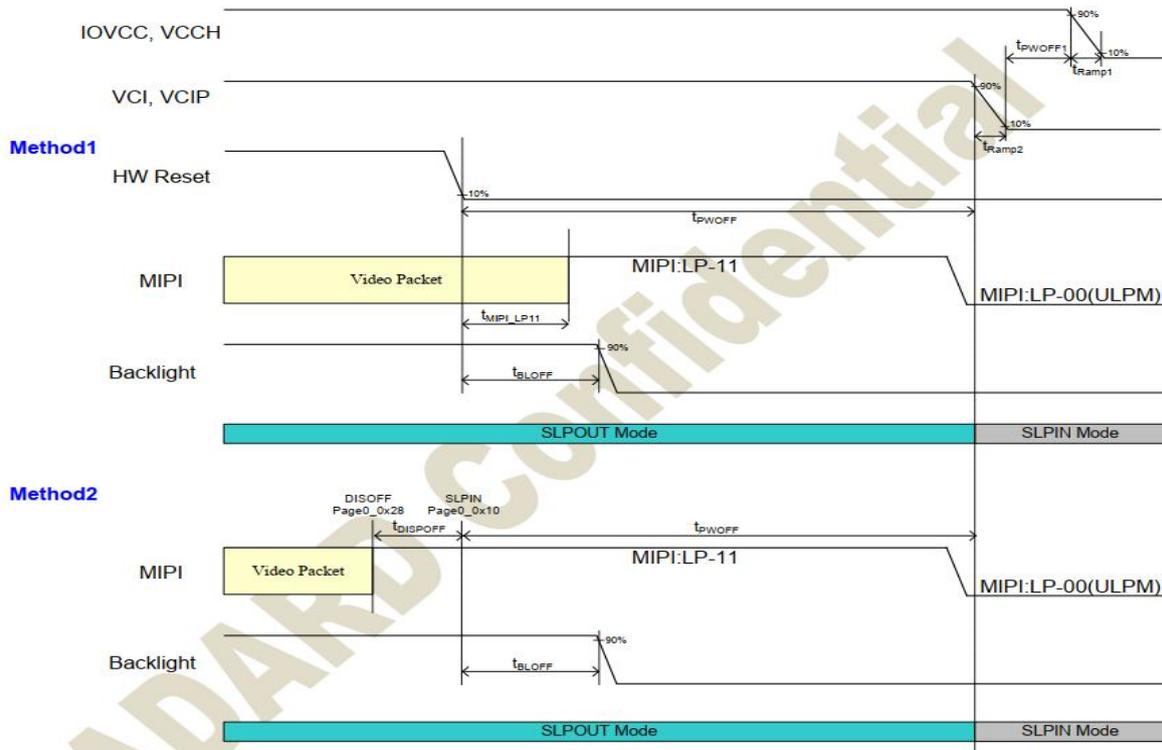


| Symbol | Value | | | Unit | Remark |
|------------------|-------|-----|-------------|------|-----------------|
| | Min | Typ | Max | | |
| t_{PWOFF1} | 0 | 5 | - | ms | |
| t_{PWOFF2} | 0 | 5 | - | ms | |
| t_{PWOFF3} | 0 | 5 | - | ms | |
| t_{PWOFF4} | 0 | 5 | - | ms | |
| t_{ramp1} | 0.2 | - | 20 | ms | IOVCC, VCCH |
| t_{ramp2} | 0.2 | - | 20 | ms | VCI, VCIP, AVDD |
| t_{ramp3} | 0.2 | - | 20 | ms | AVEE |
| t_{ramp4} | 0.2 | - | - | ms | 5Power: VGL |
| t_{ramp5} | 0.2 | - | - | ms | 5Power: VGH |
| t_{PWOFF} | 120 | - | - | ms | |
| t_{MIPI_LP11} | 0 | - | t_{PWOFF} | ms | |
| $t_{DISPOFF}$ | 50 | - | t_{PWOFF} | ms | |
| $t_{RSTHtoL}$ | 50 | - | t_{PWOFF} | ms | |
| t_{Video_OFF} | 0 | - | t_{PWOFF} | ms | |
| t_{BLOFF} | 0 | - | - | ms | |

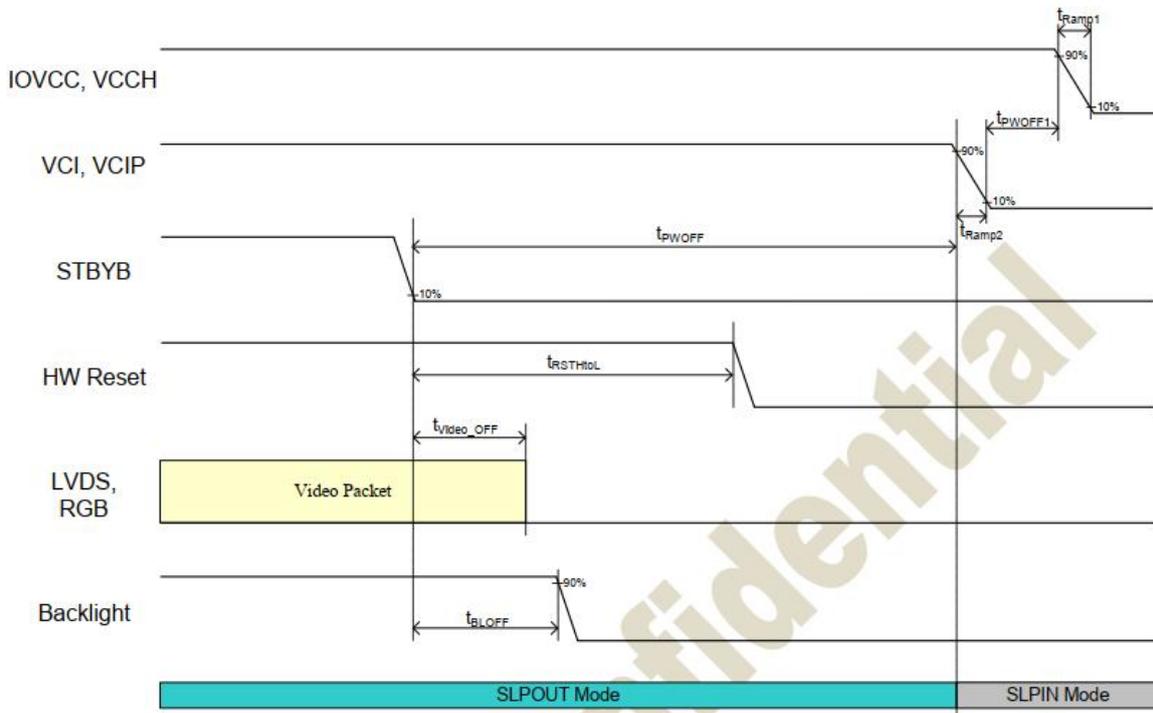
电源开闭顺序框图

The power Off sequence block diagram

2 power mode power off sequence – DSI:



2 power mode power off sequence – LVDS, RGB:



七、可靠性测试 RELIABILITY TEST

7-1 温度和湿度 Temperature and Humidity

| 测试项目 TEST ITEMS | 条件 CONDITIONS | 注释 NOTE |
|--|--------------------------|------------|
| 高温储存 High Temperature Storage | Ta=+90 °C, 120hrs | |
| 低温储存 Low Temperature Storage | Ta=-40 °C, 120hrs | |
| 高温运行试验 High Temperature Operation | Ta=+80 °C, 120hrs | |
| 低温运行试验 Low Temperature Operation | Ta=-35 °C, 120hrs | |
| 高温高湿（运行测试） High Temperature and High Humidity (Operating) | Ta=+60 °C, 90%RH, 120hrs | |

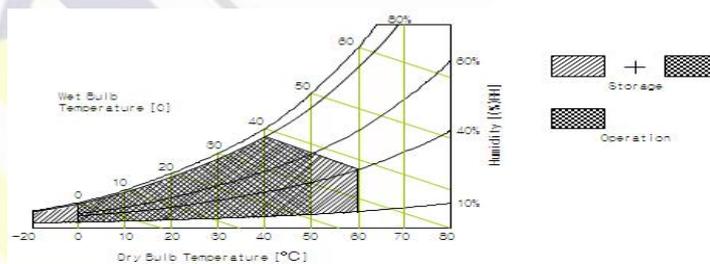
注释 Note:

1. 液晶驱动电压。由于液晶材料的特性，该电压随环境温度而变化。

Liquid Crystal driving voltage. Due to the characteristics of LC Material, this voltage varies with environmental temperature.

2. 温度和相对湿度范围如下图所示。湿球温度最高应为39°C。并且没有冷凝水。

Temperature and relative humidity range are shown in the figure below. Wet bulb temperature should be 39 °C max. and no condensation of water.



3. 产品经可靠性测试后，仅保证功能正常，无任何致命缺陷（不显示、线路缺陷、显示异常等）。

After the reliability test, the product only guarantee function normally without any fatal defect (non-display, line defect, abnormal display etc).

4. 所有显示判断均在面板温度恢复到室温2小时后进行

All display judgments are made after the panel temperature returns to room temperature for 2 hours

5. Ta: 环境温度

Ta: Ambient temperature

7-2 冲击和振动 Shock and Vibration

| 测试项目 TEST ITEMS | 条件 CONDITIONS |
|---|---|
| 包装冲击(非操作) Packing Shock (Non-Operation) | <ul style="list-style-type: none"> ● Shock level:980m/s² ● Waveform:1/2 Sine wave,6msec ● ±X, ±Y ±Z, each axis 1 times |
| 包装振动(非操作) Packing Vibration (Non-Operation) | <ul style="list-style-type: none"> ● Frequency range:8-33.3HZ ● Stoke:1.0mm ● Sweep: 10Hz-50Hz ● x, y, z 2 hours for each direction |

7-3 静电放电测试 Electrostatic Discharge

| 测试项目 TEST ITEMS | 条件 CONDITIONS |
|------------------------|---|
| ESD (Non-operation) | 150pF, 330 Ω , Contact ±4KV, Air : ±8KV. Note 1 |
| | 200pF, 0 Ω , ±200V Contact test. Note 2 |

测量点 Measure Point:

1. LCD玻璃和金属边框
LCD glass and metal bezel
2. 连接器引脚
IF connector pins

八、处理和注意事项 HANDLING & CAUTIONS

8-1 操作注意事项 Caution For Operation

◆由于液晶模组是玻璃材质，请勿对其施加强烈的机械冲击或静载荷。请小心搬运，因为冲击、振动和粗心的搬运可能会严重影响产品。如果从高处坠落或受到强烈冲击，玻璃可能碎了。

Since the LCM is made of glass, do not apply strong mechanical impact or static load onto it. Handling with care since shock, vibration, and careless handling may seriously affect the product. If it falls from a high place or receives a strong shock, the glass maybe broken.

◆在规定的电压限制内驱动 LCM 是必不可少的，因为高于限制的电压会导致 LCM 的寿命缩短。由直流引起的电化学反应会导致 LCM 出现不良劣化，因此应避免使用直流驱动。

It is indispensable to drive the LCM within the specified voltage limit since the higher voltage than the limit causes LCM's life shorter. An electro-chemical reaction due to DC causes undesirable deterioration of the LCM so that the use of DC drive should avoid.

◆请勿在电源开启时将 LCM 连接到系统或从系统断开连接。

Do not connect or disconnect the LCM to or from the system when power is on.

◆切勿在高温高湿的异常条件下使用 LCM。

Never use the LCM under abnormal conditions of high temperature and high humidity.

◆当暴露于剧烈的温度波动（热到冷或冷到热）时，LCM可能会受到影响；具体来说，从冷到热的剧烈温度波动会在 LCM 表面产生露水，这可能会影响 LCM 上偏振片的运行。

When expose to drastic fluctuation of temperature (hot to cold or cold to hot), the LCM may be affected; specifically, drastic temperature fluctuation from cold to hot, produces dew on the LCM's surface which may affect the operation of the polarizer on the LCM.

◆在低于工作温度范围的温度下，响应时间将极度延迟，另一方面，在高于其工作范围的温度下，LCM 可能会变黑。然而，这些现象并不意味着 LCM 出现故障或故障。一旦温度恢复到正常运行的推荐温度范围，LCM 将恢复正常运行。

Response time will be extremely delay at lower temperature than the operating temperature range and on the other hand LCM may turn black at temperature above its operational range. However those phenomenon do not mean malfunction or out of order with the LCM. The LCM will revert to normal operation once the temperature returns to the recommended temperature range for normal operation.

◆为防止LCD产生图像残留，在使用常黑面板时不要长时间显示固定图案。如果LCD需要显示固定图案，建议时间少于 2 分钟或者更短的时间内刷新显示内容一次或多次。强烈建议使用黑色图像或移动图像作为屏幕保护程序。

In order to prevent the LCD from producing image retention, do not display a fixed pattern for a long time when using a normally black panel. If the LCD needs to display a fixed pattern, it is recommended to refresh the display one or more times in less

than 2 minutes or less. It is strongly recommended to use a black image or moving image as a screen saver.

◆强烈的光照会导致 LCD 偏光片和彩色滤光片退化。 不准在强光或高温高湿下长期存放或直接运行。

Strong sunlight can cause LCD polarizers and color filters to degrade. Long-term storage or direct operation under strong light or high temperature and humidity is not allowed.

8-2 防静电措施 Caution Against Static Charge

◆LCM 使用 C-MOS LSI 驱动器，因此建议客户将任何未使用的输入端连接到 Vdd 或 Vss，上电前不要输入任何信号，并将您的身体、工作/装配区、装配设备接地 防止静电。

The LCM use C-MOS LSI drivers, so customers are recommended that any unused input terminal would be connected to Vdd or Vss, do not input any signals before power is turn on, and ground you body, work/assembly area, assembly equipments to protect against static electricity.

◆缓慢去除保护膜，保持去除方向与面板表面不垂直约 30 度，如有可能，在离子风机等 ESD 控制装置下，工作室湿度应保持在 50%RH 以上，以减少静电风险

Remove the protective film slowly, keeping the removing direction approximate 30-degree not vertical from panel surface, if possible, under ESD control device like ion blower, and the humidity of working room should be kept over 50%RH to reduce the risk of static charge.

◆避免使用合成纤维制成的工作服。 我们推荐棉质衣服或其他经过导电处理的纤维。

Avoid the use work clothing made of synthetic fibers. We recommend cotton clothing or other conductivity-treated fibers.

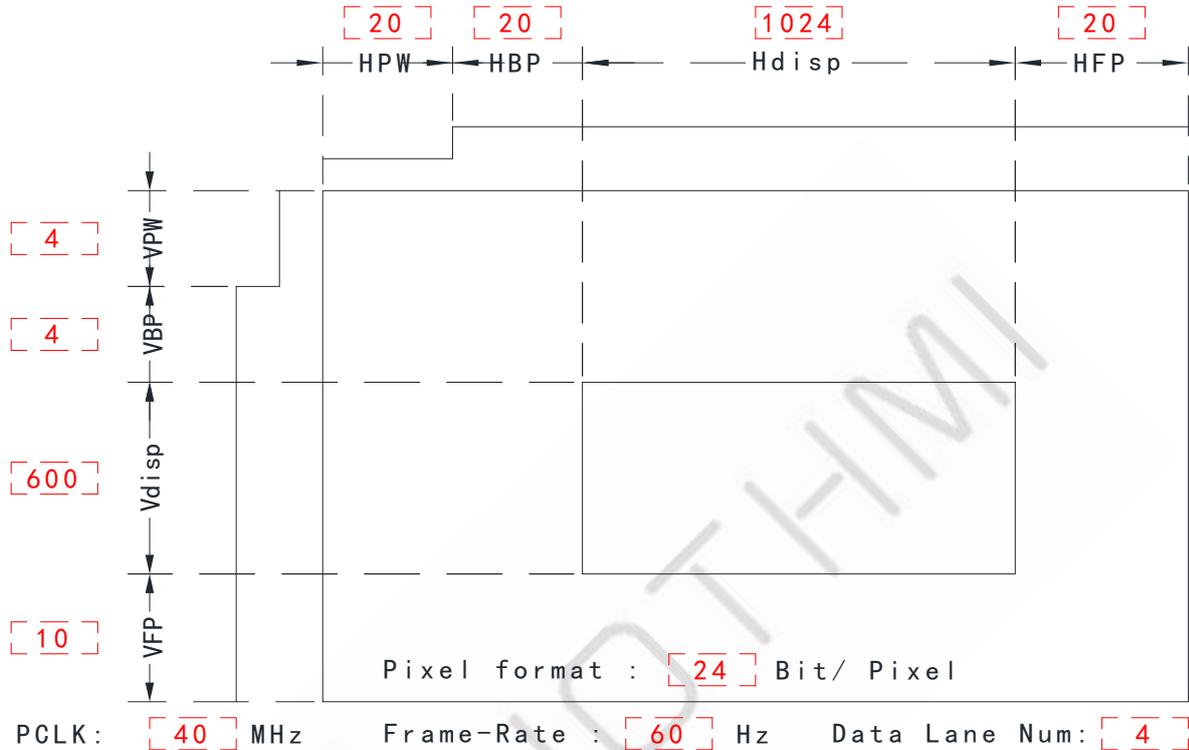
◆在处理 LCM 时，请戴上不带电材料的手套。 对地导电手腕和对地导电鞋是必需的

In handling the LCM, wear non-charged material gloves. And the conducting wrist to the earth and the conducting shoes to the earth are necessary

九、初始化代码 Initialisation code

说明：这些参数值仅供参考，可能并不完全适用于所有软件平台，请根据您使用的平台优化参数。

Description : These parameter values are for reference only and may not be fully applicable to all software platforms, please optimise the parameters according to the platform you are using.



```
Void Panel_Initial_code(void)
```

```
{
//=====上电复位操作=====//
LCD_RESET=1;
Delays(1);            //Delay 1ms
LCD_RESET=0;
Delays(10);           //Delay 10ms
LCD_RESET=1;
Delays(120);          //Delay 120ms
//=====//
```

//RGB和LVDS接口不需要初始化代码 RGB and LVDS interfaces do not require initialization code

```
SSD_SEND(0x01, 0xDF, 0x91, 0x68, 0xF9);
SSD_SEND(0x01, 0xDE, 0x00);
SSD_SEND(0x01, 0xB2, 0x00, 0x66);
SSD_SEND(0x01, 0xB3, 0x00, 0x8C);
SSD_SEND(0x01, 0xBC, 0x0F, 0x34);
SSD_SEND(0x01, 0xC1, 0x00, 0x10, 0x00, 0x00, 0x00, 0x00);
```

```

SSD_SEND (0x01, 0xBB, 0x02, 0x24, 0x0A, 0x29, 0x4F, 0x44, 0x44) ;
SSD_SEND (0x01, 0xBE, 0x1A, 0xF2) ;
SSD_SEND (0x01, 0xC3, 0x10, 0x23, 0x65, 0x23, 0x65, 0x05, 0x05, 0x05, 0x05, 0x28, 0x28, 0x31, 0x85, 0x1C,
0x05, 0x6C, 0x0A, 0x10, 0x0A, 0x10) ;
SSD_SEND (0x01, 0xC4, 0x13, 0x2C, 0x00, 0x1B, 0x0A, 0x07, 0x14) ;
SSD_SEND (0x01, 0xCE, 0x00, 0x03, 0x0F, 0x0F,
0x03, 0x03, 0x03, 0x03, 0x03, 0x03, 0x03, 0x03, 0x03) ;
SSD_SEND (0x01, 0xCF, 0x00, 0x27, 0x7C, 0x01, 0x7C, 0x01, 0x7C, 0x01, 0x7C) ;
SSD_SEND (0x01, 0xD0, 0x00, 0x1F, 0x06, 0x06, 0x04, 0x04, 0x0A, 0x0A, 0x08, 0x08, 0x1E, 0x1E, 0x17, 0x17,
0x17, 0x17, 0x1F, 0x1F, 0x01, 0x00, 0x00, 0x1F, 0x1F) ;
SSD_SEND (0x01, 0xD1, 0x00, 0x1F, 0x07, 0x07, 0x05, 0x05, 0x0B, 0x0B, 0x09, 0x09, 0x1E, 0x1E, 0x17, 0x17,
0x17, 0x17, 0x1F, 0x1F, 0x02, 0x00, 0x00, 0x1F, 0x1F) ;
SSD_SEND (0x01, 0xD2, 0x00, 0x1F, 0x09, 0x09, 0x0B, 0x0B, 0x05, 0x05, 0x07, 0x07, 0x1F, 0x1F, 0xD7, 0xD7,
0xD7, 0xD7, 0x1F, 0x1F, 0x02, 0x00, 0x00, 0x1E, 0x1E) ;
SSD_SEND (0x01, 0xD3, 0x00, 0x1F, 0x08, 0x08, 0x0A, 0x0A, 0x04, 0x04, 0x06, 0x06, 0x1F, 0x1F, 0xD7, 0xD7,
0xD7, 0xD7, 0x1F, 0x1F, 0x01, 0x00, 0x00, 0x1E, 0x1E) ;
SSD_SEND (0x01, 0xD4, 0x31, 0x03, 0x00, 0x02, 0x00, 0x07, 0x00, 0x04, 0xC2, 0x00, 0x00, 0x03, 0x03, 0x00,
0x00, 0x80, 0x06, 0xC0, 0x08, 0x03, 0x03, 0x11, 0x00, 0x00, 0x00, 0x00, 0x00, 0x00, 0x00, 0x01, 0x02, 0x04, 0x0
1, 0x04, 0xC7, 0x04, 0xC7) ;
SSD_SEND (0x01, 0xD5, 0x68, 0x73, 0x00, 0x0A, 0x08, 0x00, 0x03, 0x00, 0x04, 0x10, 0x03, 0x00, 0x03, 0x00,
0x1C, 0x77, 0x00, 0x00, 0x00) ;
SSD_SEND (0x01, 0xB7, 0x00, 0xAF, 0x00, 0x00, 0xAF, 0x00) ;
SSD_SEND (0x01, 0xC8, 0x7C, 0x66, 0x57, 0x4B, 0x47, 0x38, 0x3C, 0x26, 0x3F, 0x3E, 0x3E, 0x5C, 0x4A, 0x52,
0x44, 0x40, 0x32, 0x20, 0x06, 0x7C, 0x66, 0x57, 0x4B, 0x47, 0x38, 0x3C, 0x26, 0x3F, 0x3E, 0x3E, 0x5C, 0x4A, 0x5
2, 0x44, 0x40, 0x32, 0x20, 0x06) ;
SSD_SEND (0x01, 0xDE, 0x02) ;
SSD_SEND (0x01, 0xBB, 0x00, 0x5B, 0x5C, 0x41) ;
SSD_SEND (0x01, 0xC1, 0x15) ;
SSD_SEND (0x01, 0xD0, 0x00, 0x66, 0x66, 0x02, 0x68, 0xAE, 0x14, 0x59, 0xCD, 0x02, 0x68, 0xAE, 0x14, 0x59,
0xCD, 0x00) ;
SSD_SEND (0x01, 0xE7, 0x01) ;
SSD_SEND (0x01, 0xDE, 0x00) ;
SSD_SEND (0x01, 0x35, 0x00) ;
DCS_Short_Write_NP (0x11) ; ///Sleep Out
Delay (250) ;
DCS_Short_Write_NP (0x29) ; ///Display On
Delay (250) ;
}

```

MIPI 代码格式说明 Code Format Description

SSD_SEND (0x01, 0xCC, 0x15, 0x04, 0x35);

参数 1 (Parameter 1) : 0x00 DCS_Short_Write
 0x01 DCS_Long_Write
 0x10 Generic_Short_Write
 0x11 Generic_Long_Write

参数 2 (Parameter 2) : 寄存器地址(Register Address)

参数 3 ~ N (Parameter 3 ~ N) : 寄存器数据 (Register Data)

DCS_Short_Write_NP (0x11);

参数 1 (Parameter 1) : 寄存器地址, 无数据 (Register Address, no data)

```
Void Panel_SleepIn_Mode (void)
{
mipi.write 0x05 0x28
delay 120
mipi.write 0x05 0x10
delay 120
}
```

```
Void Panel_SleepOut_Mode (void)
{
mipi.write 0x05 0x11
delay 120
mipi.write 0x05 0x29
delay 120
}
```

文档修订记录 Document revision history :

| 版本 Version | 日期 DATE | 修改说明 Modify description | 编著 Editorial |
|---------------|------------|----------------------------|-----------------|
| 0-0 | 2025-09-08 | 初次编制 First compilation. | YL |
| | | | |

-- 结束 --

-- END --